REMARKS

In response to the Official Action mailed September 22, 2006, Applicants requests amendment of the application and reconsideration in view of the following remarks. In this Amendment, no claims are added or canceled. Claims 16 and 17 are amended to correct the inadvertent cancellation of the term "comprising" in those claims. Claims 1-21 remain pending in this application. All pending claims stand rejected.

I. 35 U.S.C. § 103 Obviousness Rejection of Claims

Claims 1-11 and 15-21 were rejected under 35 U.S.C. § 103(a) as being unpatentable over *Hetherington et al.* (U.S. Patent No. 6,275,810, hereinafter "*Hetherington*") in view of *Kaufman* (U.S. Patent No. 5,313,647). In addition, claims 12-14 were rejected under 35 U.S.C. §103(a) as being unpatentable over *Kaufman* in view of *Hetherington*. Applicants respectfully traverse the rejection of the claims.

In the Response to Arguments, the Examiner asserts that "the system virtual address [space] stores the data from context addresses, and VM_MAPIN would translate and references to virtual address when [sic] accessing data, so mapping context address would also map virtual address." Initially, Applicants note that *Kaufman* does not say that. Even if the context address space is mapped to overlay that of another process, that does not mean that the system virtual address space is mapped to overlay that of another process. The context address space and system virtual address space are unique to one another. See column 18, lines 29-36 of *Kaufman*. Moreover, it does not make sense to map the system virtual address space of one process to another because there is only one system virtual address space in *Kaufman*. See column 18, lines 35-36 of *Kaufman*.

Amendment After Final

U.S. Application No.: 09/488,909

Page 8

The Examiner further contends that VM FORK duplicates a parent process's virtual memory information, and that duplication would remove the child's virtual memory so the child would get the same virtual memory information of the parent. Applicants respectfully submit that Kaufman does not support that assertion. Kaufman does not say that the explicit virtual memory assignments are replaced with anything at all. See column 31, lines 14-45 of Kaufman. Moreover, as Applicants have repeated explained, duplication is not the same thing as overlay. See, e.g., the Appeal Brief.

Furthermore, Applicants argued that an overlay object is a file, and the term "overlay object" in Kaufman has a different meaning than "overlay" as used in the claim. The Examiner responds by asserting that the mapping overlay object is one function of the map-in process. Applicants respectfully submit that this assertion, even if supported, does not adequately rebut Applicants' contention that the overlay object of Kaufman is different than overlay as used in the present claims.

Thus, Applicants respectfully submit that *prima facie* obviousness has still not been established. Accordingly, the rejection should be withdrawn.

Claims 2-7 depend on claim 1 and are therefore patentable at least for the same reasons. Claim 15 is patentable for at least the same reasons as claim 1. Furthermore, claims 16-21 depend on claim 15 and are therefore patentable at least for the same reasons.

Applicants further submit that claims 8 and 12 are also patentable for at least the same reasons as claim 1. Just as Kaufman fails to teach or suggest "mapping a user-specific process so that it overlays virtual addresses of the master process," Kaufman also fails to teach or suggest mapping "a user-specific process to virtual addresses that mirror virtual addresses of the global

Amendment After Final

U.S. Application No.: 09/488,909

Page 9

process." Furthermore, claims 9-11 and 13-14 depend on claims 8 and 12, respectively, and are therefore patentable at least for the same reasons.

II. Conclusion

In view of the above remarks, Applicants submit that all claims are allowable over the cited prior art and respectfully request early and favorable notification to that effect.

Respectfully submitted,

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